

35 USC 101
10/073316
02/13/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10073316	FILING DATE 02/13/2002	CLASS 257	SUBCLASS 203	GAU 2811	EXAMINER P. V. W.
**APPLICANTS: Nunokawa Hideo;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-244840 08/10/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		020101			
Verified and Acknowledged Examiners's initials av					
TITLE : Semiconductor device having a condenser chip for reducing a noise					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
			Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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